IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Gopakumar Parameswaran, Cuong C. Ly, Douglas L. Yanagawa,

Mark N. Yamashita and Yuval Bachar

Assignee: Cisco Technology, Inc.

Title: SYSTEM AND METHOD FOR REDUCING CROSSTALK

BETWEEN VIAS IN A PRINTED CIRCUIT BOARD

Application No.: Unassigned Filing Date: Herewith

Examiner: Unassigned Group Art Unit: Unassigned

Docket No.: CIS0203US

Austin, Texas December 4, 2003

MAIL STOP PATENT APPLICATION COMMISSIONER FOR PATENTS P. O. BOX 1450 ALEXANDRIA, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT 37 CFR § 1.97(b)

Dear Sir:

Pursuant to 37 CFR § 1.56, § 1.97 and § 1.98, the documents listed on the accompanying PTO Form-1449 are called to the attention of the Examiner for the above patent application. Pursuant to 1276 OG 55, wherein the USPTO waived the requirement under 37 CFR 1.98 (a)(2)(i) for submitting a copy of each cited U.S. patent and each U.S. patent application publication, no copies of U.S. patents or U.S. patent applications are enclosed. Copies of any listed foreign patent documents, pending non-published U.S. patent applications, and non-patent literature are enclosed.

Citation of these documents shall not be construed as:

- 1. an admission that the documents are necessarily prior art with respect to the instant invention;
- 2. a representation that a search has been made; or
- 3. an admission that the information cited herein is, or is considered to be, material to patentability as defined in § 1.56(b).

EXPRESS MAIL NUMBER:

EV 304738421 US

Respectfully submitted,

Brenna A. Brock

Attorney for Applicant(s)

Reg. No. 48,509

Sheet 1 of 1

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U.S. Department of Commerce, Patent and Trademark Office INFORMATION DISCLOSURE STATEMENT BY APPLICANT					Atty Dock	Atty Docket No.		Serial No.	
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					Applicants	Applicants			
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